

NAN YA PLASTICS CORPORATION

SPECIFICATION OF
LCD MODULE
PRODUCT NO.: LCBHBTB61M63S_

SPEC. NO: LMB61-63A-1

CUSTOMER
APPROVED BY
DATE:

LCD DEPARTMENT
ELECTRONIC MATERIALS DIVISION
NAN YA PLASTICS CORPORATION
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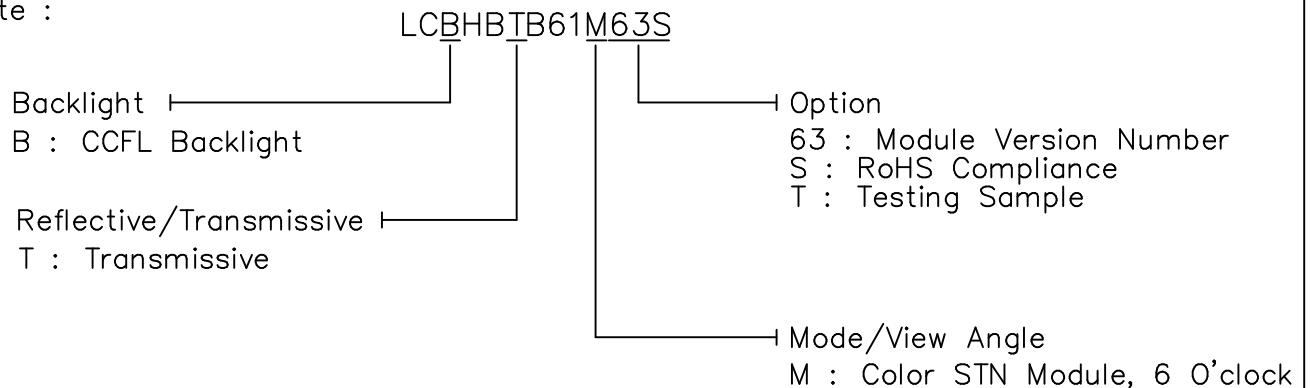
EDITED ON : Jun. 06. 2007

Q.C. DEPT.	DESIGN MANAGER	DESIGN CHECK	DESIGNER
			W.R.HSU

1. MECHANICAL DATA

NO	ITEM	CONTENTS	UNIT
1	Product No.	LCBHBTB61M63S_	-
2	Module Size	168.0 (W) x 111.0 (H) x 7.4 (D)	mm
3	Dot Size	0.10 (W) x 0.34 (H)	mm
4	Dot Pitch	0.12 (W) x 0.36 (H)	mm
5	Number of Dots	320 x RGB (W) x 240 (H)	Dot
6	Duty	1/240	-
7	LCD Display Mode	FSTN, Color STN Module	-
8	Rear Polarizer	Color Transmissive Type	-
9	Viewing Direction	6	O'clock
10	Backlight	CCFL	-
11	Controller	Excluded	-
12	DC/DC Converter	Included	-
13	Touch Panel	Excluded	-
14	Weight	210 (Approx.)	g

Note :



RoHS Compliance.

Nan Ya guarantees that this project doesn't include any materials (6 materials) or includes less than specified quantities which are regulated by RoHS Compliance.

REV/DATE	R0/ 05.18.06'					BY W.R.HSU
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2. ABSOLUTE MAXIMUM RATINGS

(1) ELECTRICAL ABSOLUTE RATINGS

VSS=0V

	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	-0.3	5.5	V	
Input Voltage	VI	-0.3	VDD	V	
Static Electricity	-	-	-	-	Note 1

Note 1 LCM should be grounded during handling LCM.

(2) ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	NORMAL TEMP.			
	OPERATING		STORAGE	
	MIN.	MAX.	MIN.	MAX.
Ambient Temperature	0	50	-20	70
Humidity (Without Condensation)	Note 2,4		Note 3,4	

Note 2 $T_a \leq 50^\circ\text{C}$: 80%RH max

Note 3 Please refer to item of reliability test

Note 4 Background color will change slightly depending on ambient temperature.
That phenomenon is reversible.

3. ELECTRICAL CHARACTERISTICS

3-1. ELECTRICAL CHARACTERISTICS OF LCM

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Logic Circuit Power Supply		VDD-VSS	Ta= 25°C	4.5	5.0	5.5	V
				3.0	3.3	3.6	
Input Voltage		VIH	H level	0.8VDD	-	VDD	V
		VIL	L level	0	-	0.2VDD	V
Contrast Adjust Voltage		VCON-VSS	Duty=1/240 VDD=3.3/5.0V 0°C 50°C	1.5	2.0	2.5	V
Supply Current for Logic		IDD	VDD-VSS = 3.3V VCON-VSS = 2.0V Ta= 25°C PATTERN: □ ■ □ ■ □ ■ □ ■ ■ □ ■ □ ■ □ ■ □	-	40	60	mA
Supply Current for Logic		IDD	VDD-VSS = 5.0V VCON-VSS = 2.0V Ta= 25°C PATTERN: □ ■ □ ■ □ ■ □ ■ ■ □ ■ □ ■ □ ■ □	-	30	45	mA
LCM	Surface Luminance	L	PATTERN: (Dots All On of White Color)	50	80	-	cd/m ²
			PATTERN: (Dots All Off)	-	4	8	
Recommended Frame Frequency for Optimum Contrast		FLM	-	115	120	125	Hz

3-2.ELECTRICAL CHARACTERISTICS OF BACKLIGHT

Used Lamp Rating

Temp.=25°C

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
Lamp Voltage	V_L	-	279	-	Vrms	-
Lamp current	I_L	-	5	-	mArms	-
Lamp power consumption	P_L	-	1.4	-	W	(*1)
Starting voltage	V_S	-	-	470	Vrms	$T_a=25^\circ\text{C}$
		-	-	580	Vrms	$T_a=0^\circ\text{C}$
Lamp life time	L_L	-	40000	-	hrs	at $I_L = 5 \text{ mArms}$ $T_a=25^\circ\text{C}$ (*2)

(*1) Power consumption excluded inverter loss .

(*2) Lamp life time is defined as follows : The final brightness is at 50% of original brightness

3-3.ELECTRICAL CHARACTERISTICS OF RECOMMENDED INVERTER TDK CXA-L10L

3-3-1 GENERAL SPECIFICATIONS

OPERATION TEMPERATURE : -10°C~60°C

STORAGE TEMPERATURE : -20°C~85°C

DIMENSION : 44.0(L)mm x 21.0(W)mm x MAX 18.0(H)mm

3-3-2 PIN ASSIGNMENTS

INPUT (CN1) CONNECTOR :

OUTPUT (CN2) CONNECTOR :

NO.	FUNCTION
1	VIN
2	GND

NO.	FUNCTION
3	OUT1
4	OUT2
5	OUT GND

3-3-3 RELATIONSHIP BETWEEN VIN & TUBE CURRENT

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
Input Voltage	VIN	-	10.1	-	V	
Tube Current	IL	-	5	-	mA	

4. OPTICAL CHARACTERISTICS

4-1. Optical Char. of Normal Temp. Mode

AT V_{OP}

ITEM MODE		Cr(Contrast Ratio)						θ (Viewing Angle)		ϕ (Viewing Angle)	
		0°C		25°C		50°C		25°C		25°C	
		MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.
T	M	14	20	17	25	3.5	5	-	F: >60 R: 35	-	L: 45 R: 45
NOTE		NOTE 6						NOTE 5			

NOTE :

T : Transmissive

M : Color STN Module, 6 O'clock

AT $\phi=0^\circ$ $\theta=0^\circ$

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Response Time (rise)	Tr	0°C	640	800	1200	ms	NOTE 2
		25°C	240	300	450		
		50°C	80	100	150		
Response Time (fall)	Tf	0°C	360	450	670	ms	NOTE 2
		25°C	80	100	150		
		50°C	48	60	90		

4-2. Color of CIE Coordinate

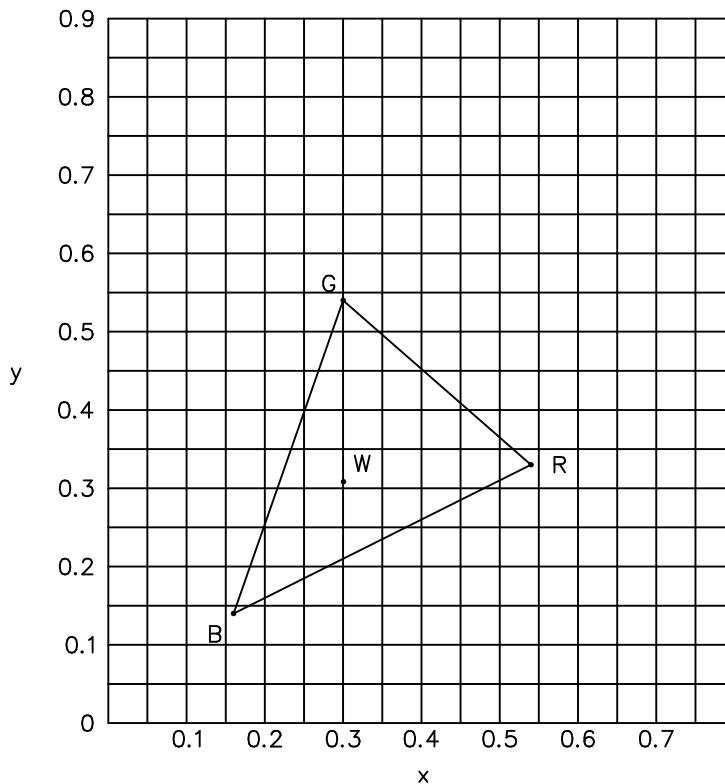
Ta = 25°C

ITEM		SYMBOL	CONDITION	VALUE	NOTE
Color of CIE Coordinate	Red	X	$\phi=0^\circ, \theta=0^\circ$	0.54	Note*
		y		0.33	
	Green	X	$\phi=0^\circ, \theta=0^\circ$	0.30	
		y		0.54	
	Blue	X	$\phi=0^\circ, \theta=0^\circ$	0.16	
		y		0.14	
	White	X	$\phi=0^\circ, \theta=0^\circ$	0.30	
		y		0.31	

Note* Measuring at position 3 on Fig.1
 CIE chromaticity diagram

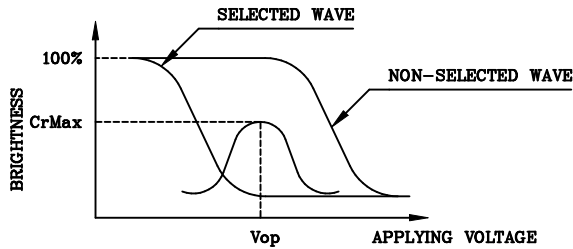
Tolerance : ± 0.05

Fig.1

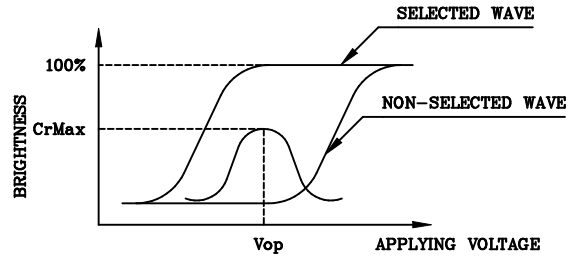


(NOTE 1)

Definition of Operation Voltage(Vop)



(positive type)



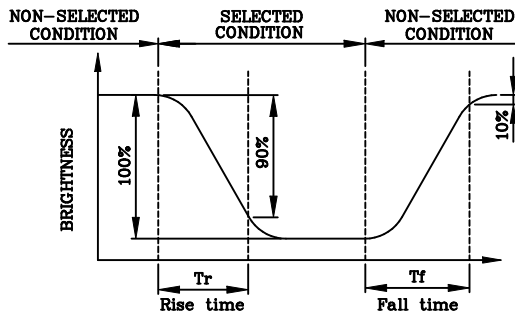
(negative type)

*Conditions

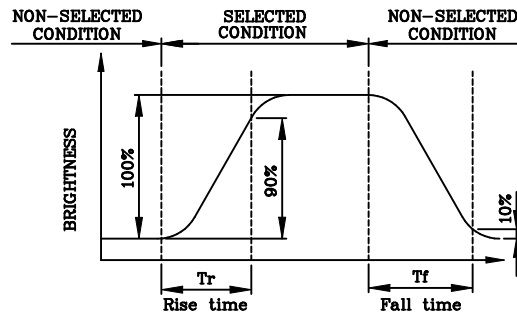
Viewing Angle : 0
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias

(NOTE 2)

Definition of Response Time(Tr,Tf)



(positive type)



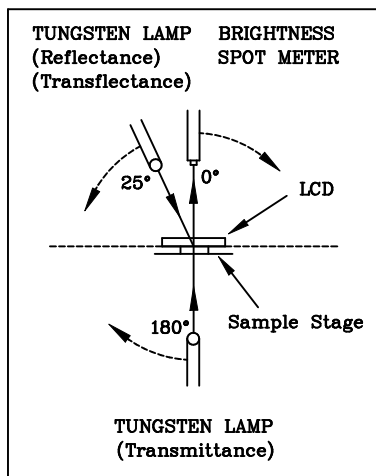
(negative type)

*Conditions

Operating Voltage : Vop
Viewing Angle (θ,φ) : (0,0)
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias

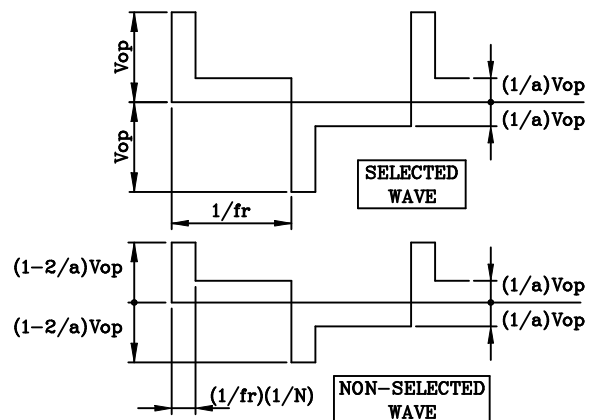
(NOTE 3)

Description of Measuring Equipment and Driving Waveforms



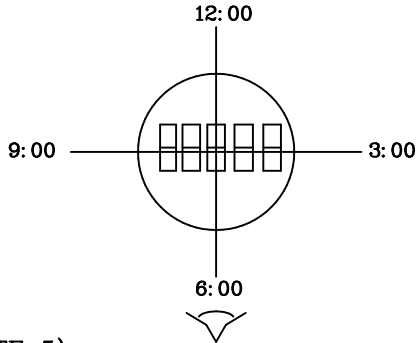
CONST.
TEMP.
CHAMBER

Multiplex Driving (1/N duty 1/a bias)



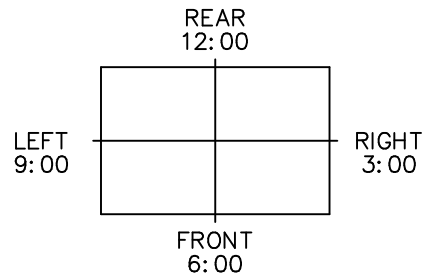
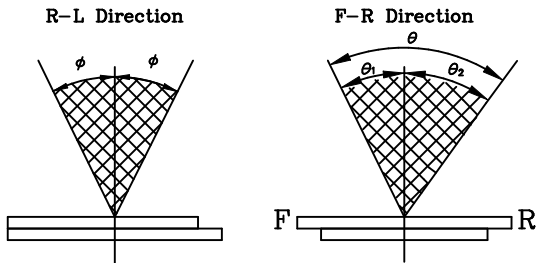
(NOTE 4)

Definition of Viewing Direction



(NOTE 5)

Definition of Viewing Angle



*For This Product
 The Viewing Direction Is 6 O'clock
 So $\theta_1 > \theta_2$

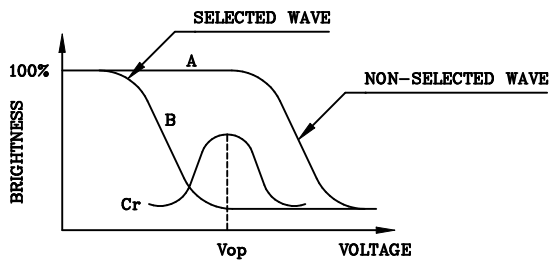
$$\theta = \theta_1 + \theta_2$$

*Conditions

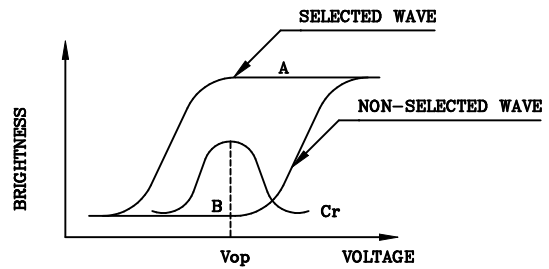
Operating Voltage : V_{op}
 Frame Frequency : 120Hz
 Applying Waveform : 1/N duty 1/a bias
 Contrast Ratio : larger than 2

(NOTE 6)

Definition of Contrast Ratio (Cr)



(positive type)



(negative type)

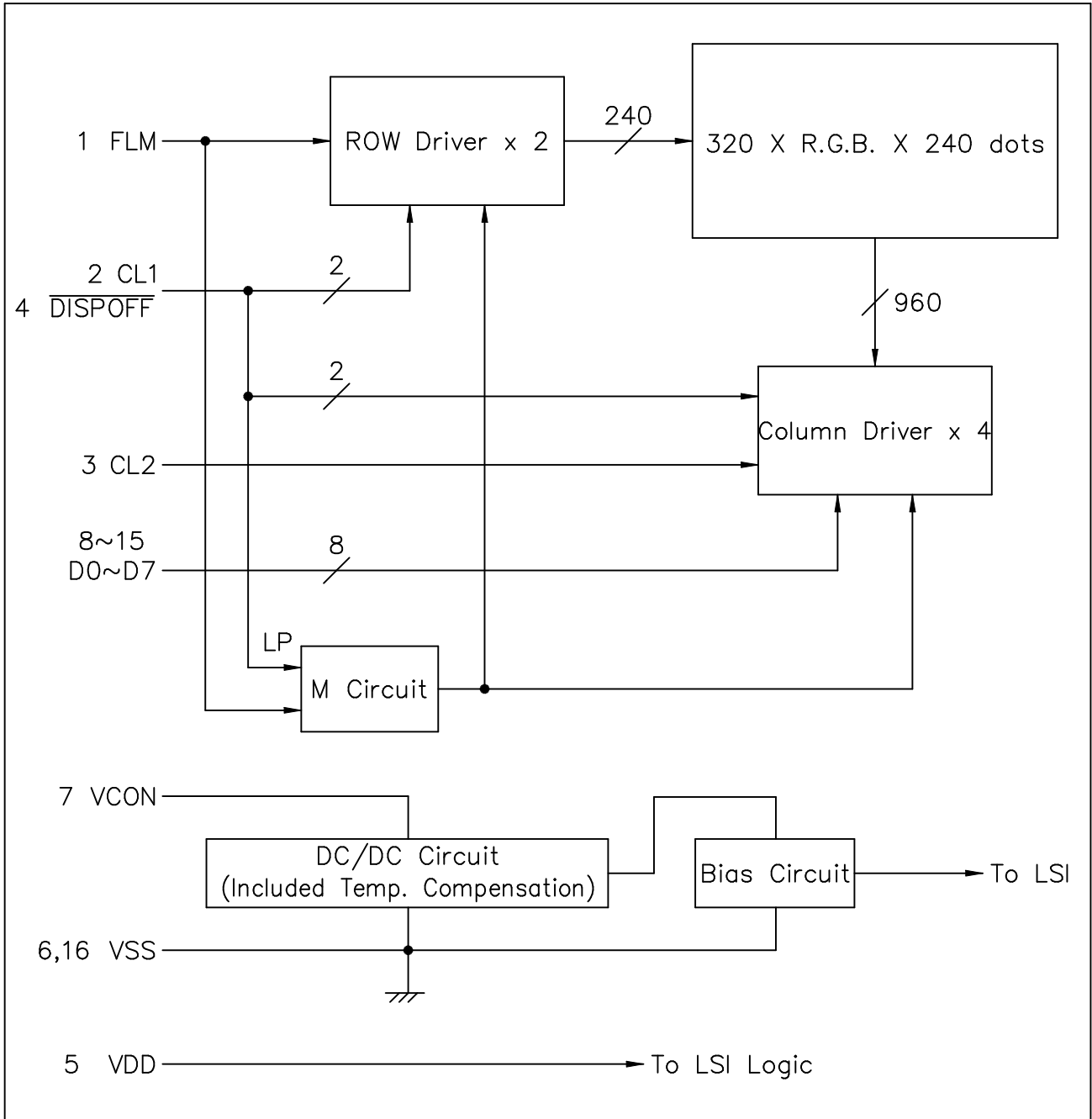
$$\text{Contrast Ratio : } Cr = A/B$$

*Conditions

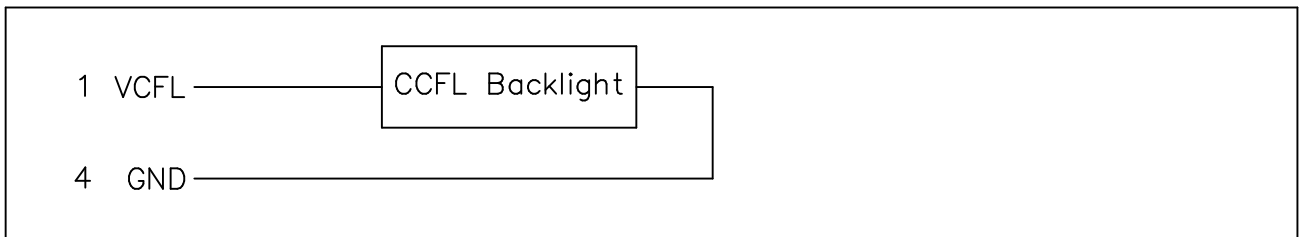
Viewing Angle : 0
 Frame Frequency : 120Hz
 Applying Waveform : 1/N duty 1/a bias

5. BLOCK DIAGRAM

LCD



CCFL



6. INTERNAL PIN CONNECTION

LCD

Pin No.	Symbol	Level	Function
1	FLM	H/L	First Line Marker
2	CL1	H→L	Data Latch Signal
3	CL2	H→L	Clock Signal for Shifting Data
4	<u>DISPOFF</u>	H/L	Display Control Signal, H :Display on L :Display off
5	VDD	-	Power Supply for Logic
6	VSS	-	Power Supply (0V,GND)
7	VCON	-	Contrast Adjust
8	D0	H/L	Display Data
9	D1	H/L	Display Data
10	D2	H/L	Display Data
11	D3	H/L	Display Data
12	D4	H/L	Display Data
13	D5	H/L	Display Data
14	D6	H/L	Display Data
15	D7	H/L	Display Data
16	VSS	-	Power Supply (0V,GND)

LCD INTERFACE CABLE :

FFC,N16,Pitch 1.0 mm (Thickness = 0.3 mm)

MATING CONNECTOR :

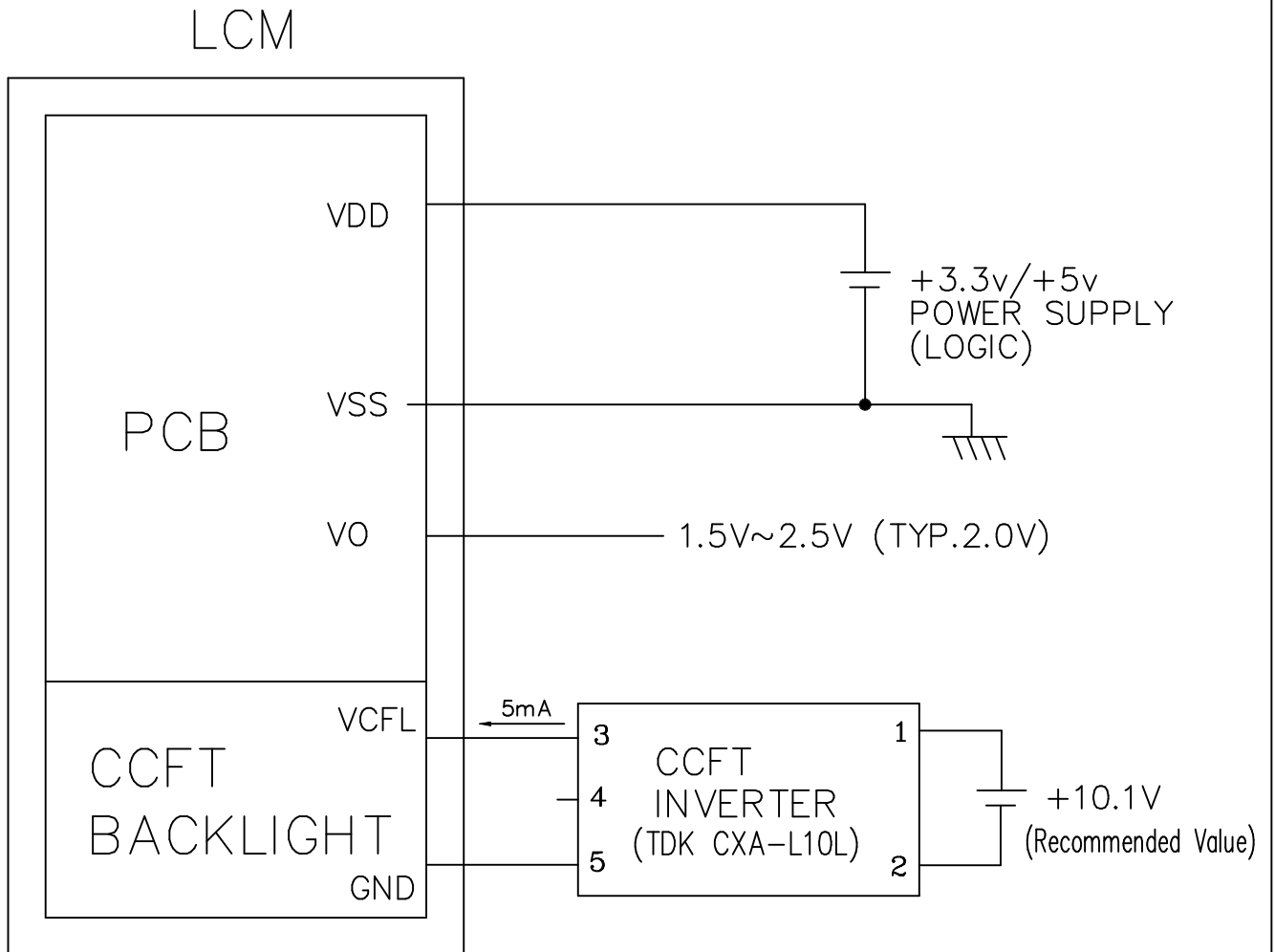
MOLEX 52207-1685 or COMPATIBLE

CCFL

Pin No.	Symbol	Level	Function
1	HOT	-	Power Supply for CCFL(HOT)
2	NC	-	Non-connection
3	NC	-	Non-connection
4	GND	-	Power Supply for CCFL(GND)

CCFL CONNECTOR : JAE/IL-G-4S-S3C2

7. POWER SUPPLY

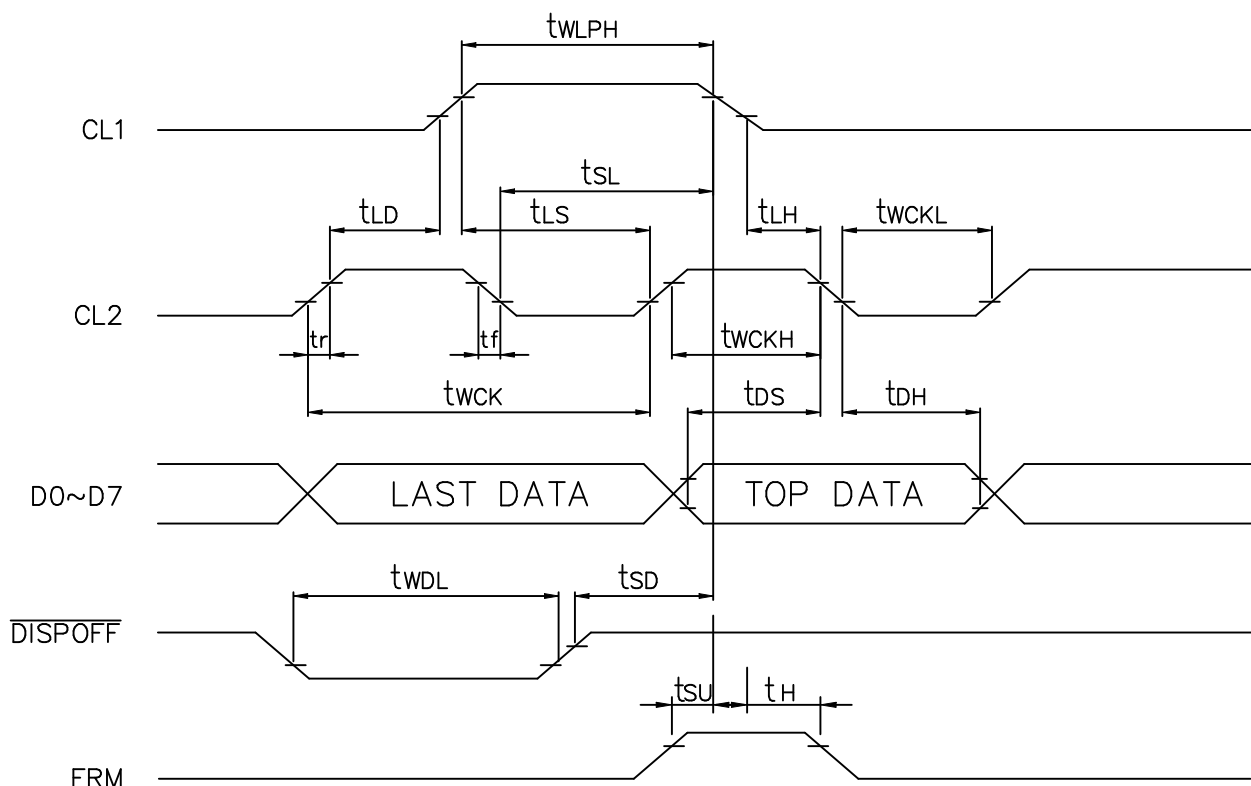


8. TIMING CHARACTERISTICS

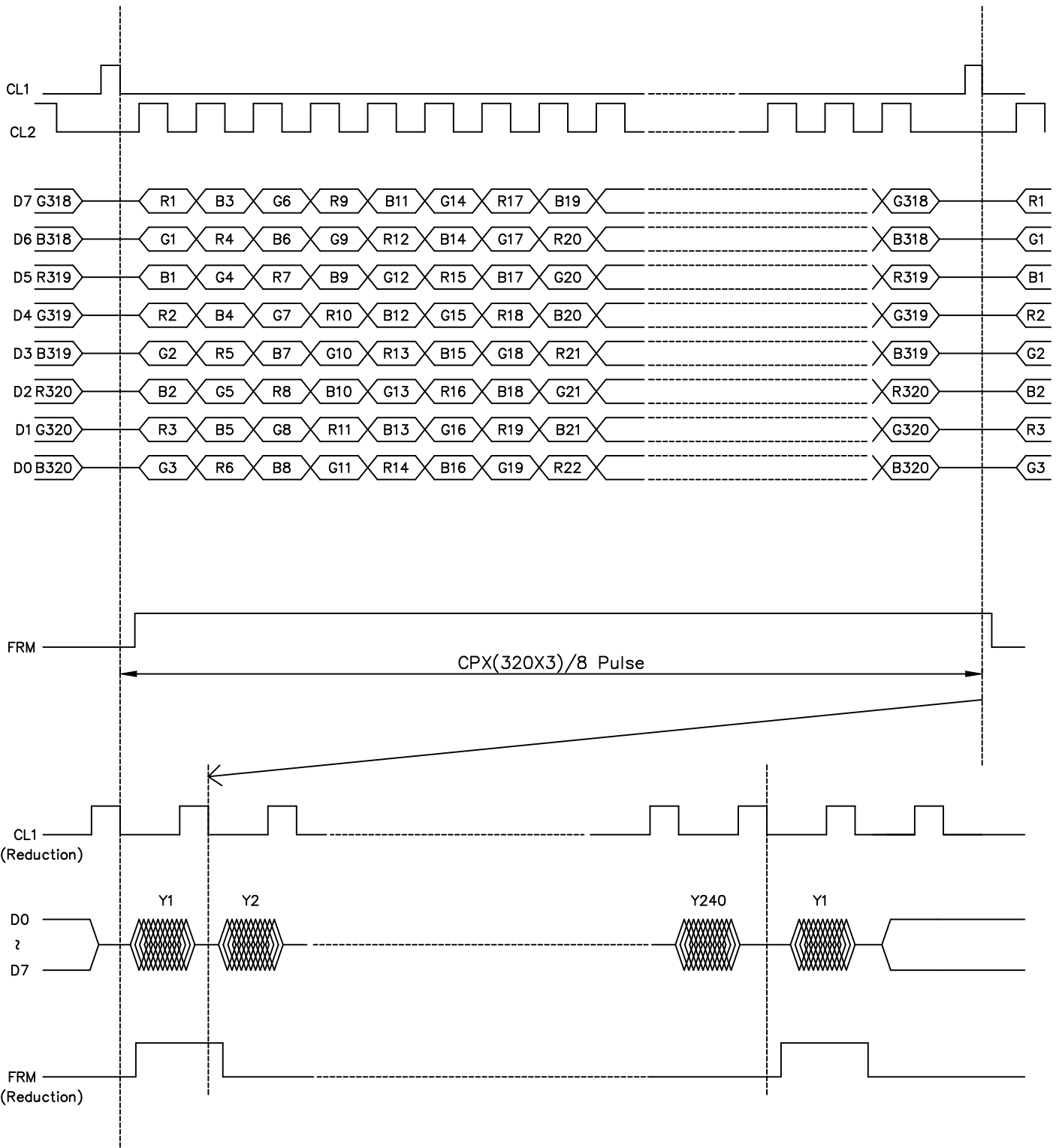
8-1. INTERFACE TIMING

VDD=3.3/5.0V ± 10%

Parameter	SYMBOL	MIN.	MAX.	UNIT
CLOCK PULSE CYCLE TIME	t_{wck}	66/40	-	ns
CLOCK PULSE HIGH LEVEL WIDTH	t_{wckH}	23/12	-	ns
CLOCK PULSE LOW LEVEL WIDTH	t_{wckL}	23/14	-	ns
LATCH PULSE HIGH LEVEL WIDTH	t_{wLPH}	30/15	-	ns
CL2→CL1 RISE TIME	t_{LD}	10/5	-	ns
CL2→CL1 FALL TIME	t_{SL}	30/25	-	ns
CL1→CL2 RISE TIME	t_{LS}	30/25	-	ns
CL1→CL2 FALL TIME	t_{LH}	30/25	-	ns
CLOCK PULSE RISE/FALL TIME	t_r, t_f	-	50	ns
DATA SETUP TIME	t_{DS}	10/5	-	ns
DATA HOLD TIME	t_{DH}	25/15	-	ns
DISPOFF LOW LEVEL WIDTH	t_{WDL}	1.2	-	μs
DISPOFF CANCELLATION TIME	t_{SD}	100	-	ns
FLM SETUP TIME	t_{SU}	30	-	ns
FLM HOLD TIME	t_H	50	-	ns

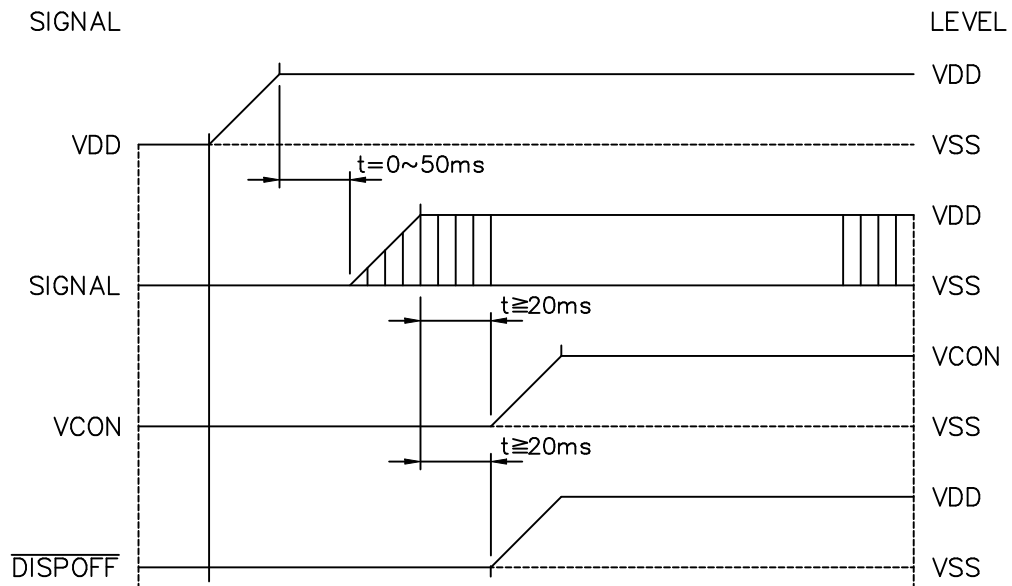


8-2. TIMING CHART OF INPUT SIGNAL

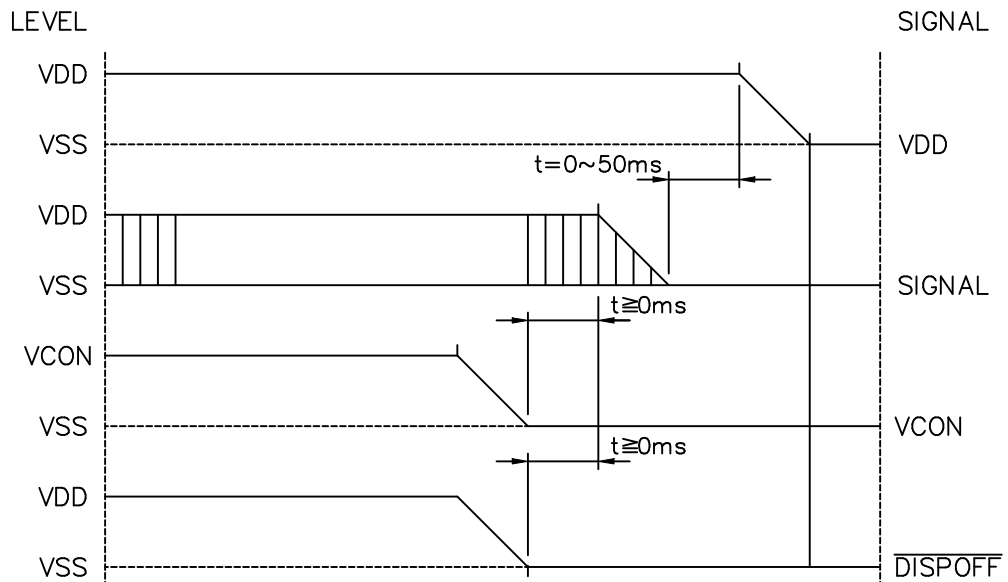


8-3. POWER ON/OFF TIMING

ON SEQUENCE

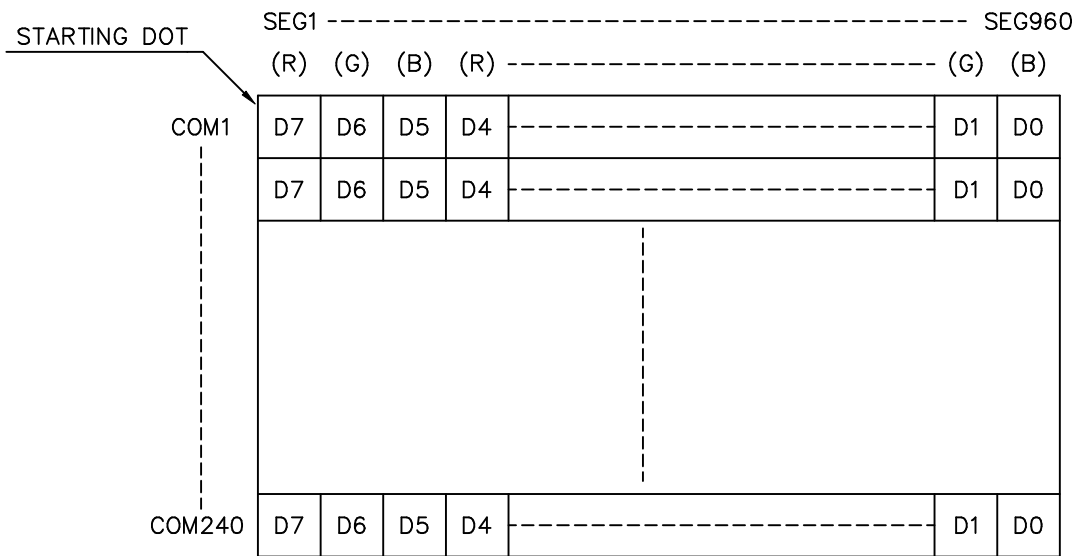


OFF SEQUENCE



Please maintain the above sequence when turning on and off the power supply of the module. If $\overline{\text{DISPOFF}}$ is supplied to the module while internal alternate signal for LCD driving(M) is unstable, DC component will be supplied to the LCD panel. This may cause damage the LCD module.

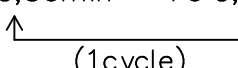
8-4.DISPLAY PATTERN



D0~D7 are 8 bits transmitted data, where D0 is LSB and D7 is MSB.

9. RELIABILITY TEST

WIDE TEMPERATURE RELIABILITY TEST

NO	ITEM	CONDITION			STANDARD	NOTE
1	High Temp. Storage	80°C	120Hrs		Appearance without defect	
2	Low Temp. Storage	-40°C	120Hrs		Appearance without defect	
3	High Temp. & High Humi. Storage	60°C 90%RH	120Hrs		Appearance without defect	
4	High Temp. Operating Display	70°C	120Hrs		Appearance without defect	
5	Low Temp. Operating Display	-20°C	120Hrs		Appearance without defect	
6	Thermal Shock	-20°C, 30min → 70°C, 30min  (1cycle)			Appearance without defect	10 cycles

Inspection Provision

1. Purpose

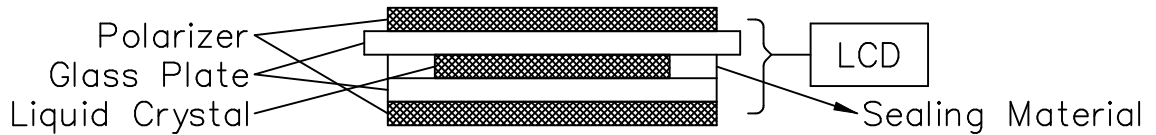
The NAN YA inspection provision provides outgoing inspection provision and its expected quality level based on our outgoing inspection of NAN YA LCD produces.

2. Applicable Scope

The NAN YA inspection provision is applicable to the arrangement in regard to outgoing inspection and quality assurance after outgoing.

3. Technical Terms

3-1 NAN YA Technical Terms



4. Outgoing Inspection

4-1 Inspection Method

MIL-STD-105E Level II Regular inspection

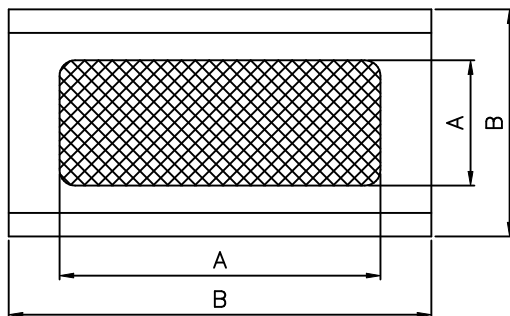
4-2 Inspection Standard

	Item		AQL(%)	Remarks
Major Defect	Dots	Opens Shorts Erroneous operation	0.4	faults which substantially lower the practicality and the initial purpose difficult to achieve.
	Solder appearance	Shorts Loose		
	Cracks	Display surface cracks		

	Dimensions	External from Dimensions	0.4	
Minor Defect	Inside the glass	Black spots	0.65	faults which appear to pose almost no obstacle to the practicality, effective use, and operation.
	Polarizing plate	Scratches, foreign Matter, air bubbles, and peeling		
	Dots	Pinhole, deformation		
	Color tone	Color unevenness		
	Solder appearance	Cold solder Solder projections		

4-3 Inspection Provisions
*Viewing Area Definition

Fig. 1



A : Zone Viewing Area
B : Zone Glass Plate Outline

*Inspection place to be 500 to 1000 lux illuminance uniformly without glaring.
The distance between luminous source(daylight fluorescent lamp and cool white fluorescent lamp) and sample to be 30cm to 50cm.

*Test and measurement are performed under the following conditions, unless otherwise specified.

Temperature 20± 15°C
Humidity 65± 20%R.H.
Pressure 860~1060hPa(mmbar)

In case of doubtful judgment, it is performed under the following conditions.

Temperature 20± 2°C
Humidity 65± 5%R.H.
Pressure 860~1060hPa(mmbar)

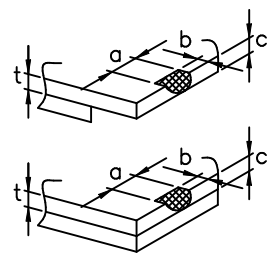
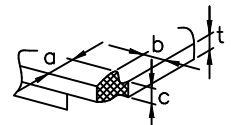
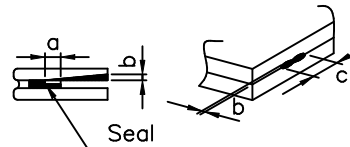
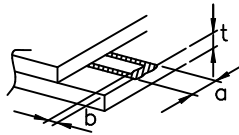
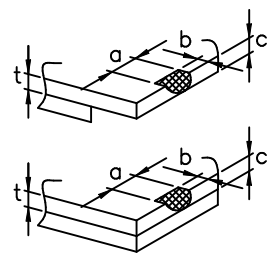
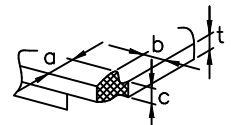
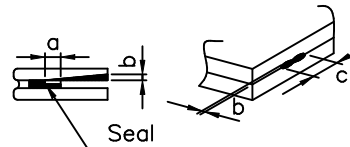
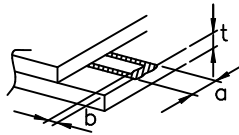
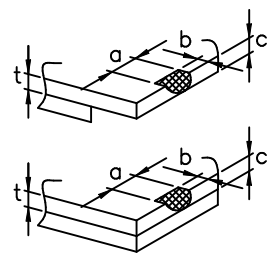
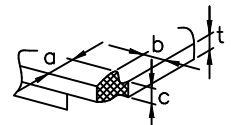
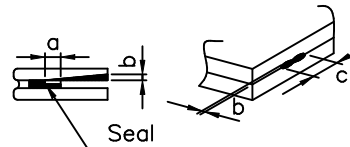
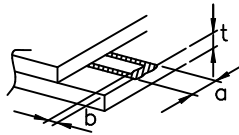
5.Specification for quality check
5-1 Electrical characteristics

NO.	Item	Criterion
1	Non operational	Fail
2	Miss operating	Fail
3	Missing dot	Fail
4	Contrast irregular	Fail
5	Response time	Within Specified value
6	Backlight turn on/off	Within Specified value

5-2 External Appearance Defect

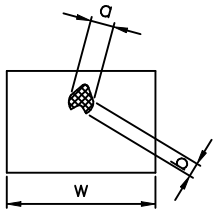
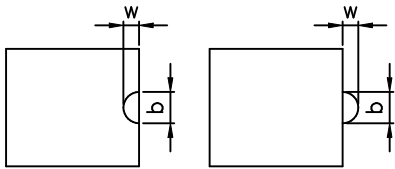
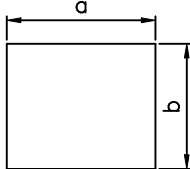
NO.	Item	Criterion																							
1	Black spots, foreign matter, and white spots (Including light leakage due to pinholes of polarizing plates, etc.)	<p>(1)-1-Spots</p> <table border="1" data-bbox="703 488 1401 819"> <thead> <tr> <th>Average Diameter(mm):D</th> <th>Number of pieces permitted</th> <th>Minimum Space</th> </tr> </thead> <tbody> <tr> <td>$D \leq 0.2$</td> <td>Ignore</td> <td>-</td> </tr> <tr> <td>$0.2 < D \leq 0.3$</td> <td>5</td> <td>10mm</td> </tr> <tr> <td>$0.3 < D \leq 0.4$</td> <td>2</td> <td>30mm</td> </tr> <tr> <td>$0.4 < D$</td> <td>0</td> <td></td> </tr> </tbody> </table> <p>Number of total pieces is set to within 5 pieces.</p> <p>Note that when there are 2 pieces or more, they are not to be concentrated. Set as: Average diameter = (Long diameter + Short diameter)/2</p> <p>(1)-2-Blurred Spots(At lighting condition)</p> <table border="1" data-bbox="703 1234 1337 1469"> <thead> <tr> <th>Average Diameter(mm):D</th> <th>Number of pieces permitted</th> </tr> </thead> <tbody> <tr> <td>$D \leq 0.3$</td> <td>Ignore</td> </tr> <tr> <td>$0.3 < D \leq 0.75$</td> <td>5</td> </tr> <tr> <td>$0.75 < D$</td> <td>0</td> </tr> </tbody> </table> <p>Number of total pieces is set to within 5 pieces.</p> <p>Note that when there are 2 pieces or more, they are not to be concentrated. Set as: Average diameter = (Long diameter + Short diameter)/2</p>	Average Diameter(mm):D	Number of pieces permitted	Minimum Space	$D \leq 0.2$	Ignore	-	$0.2 < D \leq 0.3$	5	10mm	$0.3 < D \leq 0.4$	2	30mm	$0.4 < D$	0		Average Diameter(mm):D	Number of pieces permitted	$D \leq 0.3$	Ignore	$0.3 < D \leq 0.75$	5	$0.75 < D$	0
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1	Line	<p>(1)-1-Lines</p> <table border="1" data-bbox="703 443 1431 721"> <thead> <tr> <th>Width(mm): W</th> <th>Length(mm): L</th> <th>Number of pieces permitted</th> </tr> </thead> <tbody> <tr> <td>$W \leq 0.03$</td> <td>Ignore</td> <td>Ignore</td> </tr> <tr> <td>$0.03 < W \leq 0.08$</td> <td>$L \leq 4$</td> <td>2</td> </tr> <tr> <td>$0.08 < W \leq 0.1$</td> <td>$L \leq 1$</td> <td>1</td> </tr> </tbody> </table> <p>Object exceeding 0.1mm follow the standards of the spots form. Note that when there are 2 pieces or more, they are not to be concentrated.</p> <p>(1)-2-Blurred Lines(At lighting condition)</p> <table border="1" data-bbox="703 1021 1431 1299"> <thead> <tr> <th>Width(mm): W</th> <th>Length(mm): L</th> <th>Number of pieces permitted</th> </tr> </thead> <tbody> <tr> <td>$W \leq 0.03$</td> <td>Ignore</td> <td>Ignore</td> </tr> <tr> <td>$0.03 < W \leq 0.08$</td> <td>$L \leq 3$</td> <td>6</td> </tr> <tr> <td>$0.08 < W$</td> <td>$3 < L$</td> <td>None</td> </tr> </tbody> </table> <p>Object exceeding 0.1mm follow the standards of the spots form. Note that when there are 2 pieces or more, they are not to be concentrated.</p>	Width(mm): W	Length(mm): L	Number of pieces permitted	$W \leq 0.03$	Ignore	Ignore	$0.03 < W \leq 0.08$	$L \leq 4$	2	$0.08 < W \leq 0.1$	$L \leq 1$	1	Width(mm): W	Length(mm): L	Number of pieces permitted	$W \leq 0.03$	Ignore	Ignore	$0.03 < W \leq 0.08$	$L \leq 3$	6	$0.08 < W$	$3 < L$	None
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2	Scratches(Glass, reflection plates, and polarizing plates)	In accordance with black spots. (At non lighting condition)																								
3	Color irregular	Not remarkable color irregular.																								

4	Air bubbles polarizing plates, and reflection plates	<table border="1" data-bbox="702 392 1204 672"> <tr> <td data-bbox="702 392 954 533">Average Diameter (mm): D</td> <td data-bbox="954 392 1204 533">Number of pieces permitted</td> <td data-bbox="1204 392 1452 672" rowspan="2">Average diameter = (Long diameter + Short diameter)/2</td> </tr> <tr> <td data-bbox="702 533 954 672">D ≤ 0.3 0.3 < D</td> <td data-bbox="954 533 1204 672">Ignore 0</td> </tr> </table> <p data-bbox="702 683 1452 772">Note that when there are 4 pieces or more, they are not to be concentrated.</p>		Average Diameter (mm): D	Number of pieces permitted	Average diameter = (Long diameter + Short diameter)/2	D ≤ 0.3 0.3 < D	Ignore 0					
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5	Cracks	<table border="1" data-bbox="654 784 1452 1946"> <tr> <td data-bbox="654 784 1045 1164"> <p data-bbox="654 784 1045 828">(1) General crack</p>  </td> <td data-bbox="1045 784 1452 1164"> <p data-bbox="1045 784 1452 828">$a \leq 5$</p> <p data-bbox="1045 828 1452 873">$b \leq 2$</p> <p data-bbox="1045 873 1452 918">$c \leq t$</p> <p data-bbox="1045 918 1452 1164">Where, a and b are ignored when less than or equal to 0.5 The numbers of pieces are set at up to 5 pieces.</p> </td> </tr> <tr> <td data-bbox="654 1164 1045 1355"> <p data-bbox="654 1164 1045 1209">(2) Corner crack</p>  </td> <td data-bbox="1045 1164 1452 1355"> <p data-bbox="1045 1164 1452 1209">$a \leq 2.5$</p> <p data-bbox="1045 1209 1452 1254">$b \leq 2.5$</p> <p data-bbox="1045 1254 1452 1299">$c \leq t$</p> <p data-bbox="1045 1299 1452 1355">$a + b \leq 4$</p> </td> </tr> <tr> <td data-bbox="654 1355 1045 1624"> <p data-bbox="654 1355 1045 1400">(3) Seal portion crack</p>  </td> <td data-bbox="1045 1355 1452 1624"> <p data-bbox="1045 1355 1452 1400">$a \leq$ The seal width $\times 1/3$</p> <p data-bbox="1045 1400 1452 1444">$b \leq t \times 2/3$</p> <p data-bbox="1045 1444 1452 1489">$c \leq 5$</p> <p data-bbox="1045 1489 1452 1624">The numbers of pieces are set at up to 5 pieces.</p> </td> </tr> <tr> <td data-bbox="654 1624 1045 1848"> <p data-bbox="654 1624 1045 1668">(4) ITO Pin crack</p>  </td> <td data-bbox="1045 1624 1452 1848"> <p data-bbox="1045 1624 1452 1668">$a \leq 5$</p> <p data-bbox="1045 1668 1452 1713">$b \leq 1/3$ pin length</p> <p data-bbox="1045 1713 1452 1758">$c \leq t$</p> </td> </tr> <tr> <td data-bbox="654 1848 1045 1946"> <p data-bbox="654 1848 1045 1892">(5) Progressive cracks</p> </td> <td data-bbox="1045 1848 1452 1946"> <p data-bbox="1045 1848 1452 1946">All taken to be unacceptable.</p> </td> </tr> </table>		<p data-bbox="654 784 1045 828">(1) General crack</p> 	<p data-bbox="1045 784 1452 828">$a \leq 5$</p> <p data-bbox="1045 828 1452 873">$b \leq 2$</p> <p data-bbox="1045 873 1452 918">$c \leq t$</p> <p data-bbox="1045 918 1452 1164">Where, a and b are ignored when less than or equal to 0.5 The numbers of pieces are set at up to 5 pieces.</p>	<p data-bbox="654 1164 1045 1209">(2) Corner crack</p> 	<p data-bbox="1045 1164 1452 1209">$a \leq 2.5$</p> <p data-bbox="1045 1209 1452 1254">$b \leq 2.5$</p> <p data-bbox="1045 1254 1452 1299">$c \leq t$</p> <p data-bbox="1045 1299 1452 1355">$a + b \leq 4$</p>	<p data-bbox="654 1355 1045 1400">(3) Seal portion crack</p> 	<p data-bbox="1045 1355 1452 1400">$a \leq$ The seal width $\times 1/3$</p> <p data-bbox="1045 1400 1452 1444">$b \leq t \times 2/3$</p> <p data-bbox="1045 1444 1452 1489">$c \leq 5$</p> <p data-bbox="1045 1489 1452 1624">The numbers of pieces are set at up to 5 pieces.</p>	<p data-bbox="654 1624 1045 1668">(4) ITO Pin crack</p> 	<p data-bbox="1045 1624 1452 1668">$a \leq 5$</p> <p data-bbox="1045 1668 1452 1713">$b \leq 1/3$ pin length</p> <p data-bbox="1045 1713 1452 1758">$c \leq t$</p>	<p data-bbox="654 1848 1045 1892">(5) Progressive cracks</p>	<p data-bbox="1045 1848 1452 1946">All taken to be unacceptable.</p>
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6	Outer dimensions	Should be within the tolerance.
7	Newton ring(touch panel)	Orbicular of interference fringes is not allowed in the optimum contrast within the active area under viewing angle.
8	Soldering	Should be no defective soldering such as shorting, loose terminal cold solder, peeling of printed circuit board pattern, improper mounting position, etc.

5-3 Dot Appearance Defect

NO.	Item	Criteria
1	Pinhole	 <p>Dot display a and b are each $\leq 0.2\text{mm}$ The overall total is taken be with in 10 units. Note that they are not to be concentrated.</p>
2	Missing	 <p>Dot display a and b are each $\leq 0.2\text{mm}$ The overall total is taken to be with in 10 units.</p>
3	Thick and thin display	 <p>Taken to be within $\pm 1.5\%$ of display character width(a) and height(b).</p>

NOTICE:

- SAFETY

- 1.If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 2.If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

- HANDLING

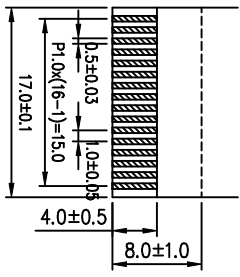
- 1.Avoid static electricity which can damage the CMOS LSI.
- 2.Do not remove the panel or frame from the module.
- 3.The polarizing plate of the display is very fragile. So, please handle it very carefully.
- 4.Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.Do not use ketonics solvent & Aromatic solvent. Use a soft cloth soaked with a cleaning naphtha solvent.

- STORAGE

- 1.Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C}\pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 2.Do not place the module near organics solvents or corrosive gases.
- 3.Do not crush, shake, or jolt the module.

- TERMS OF WARRANT

- 1.Acceptance inspection period
The period is within one month after the arrival of contracted commodity at the buyer's factory site.
- 2.Applicable warrant period
The period is within twelve months since the date of shipping out under normal using and storage conditions.



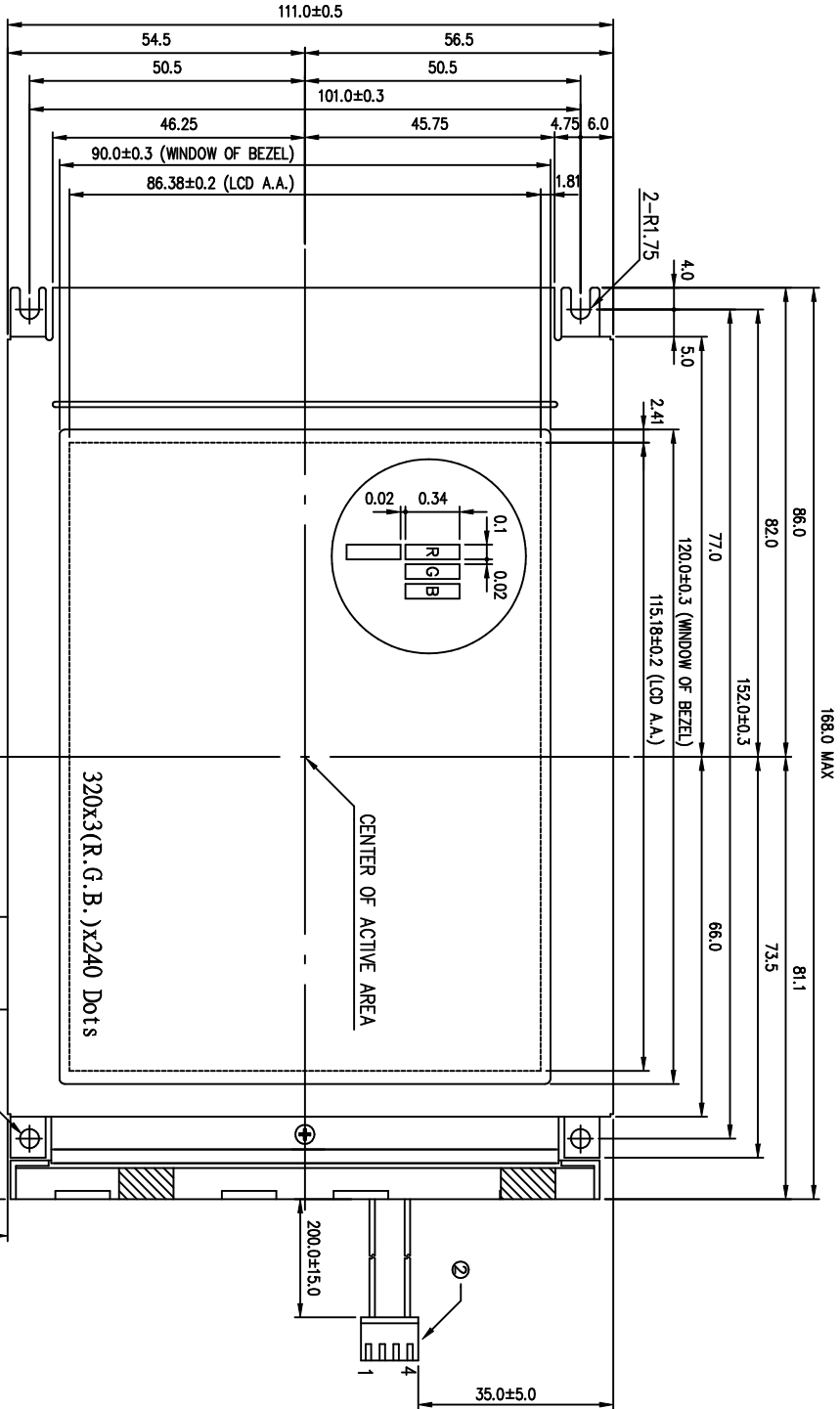
"A" DETAIL
S=2:1

① PIN ASSIGNMENT OF I/O CONNECTION

Pin No.	SYMBOL	LEVEL	FUNCTION
1	FLM	H	First Line Marker
2	CI1	H~L	Data Latch
3	CI2	H~L	Data Shift
4	DISPOFF	H/L	H : On L : Off
5	VDD	-	Power Supply for Logic
6	VSS	-	GND
7	VCON	-	Contrast adjust
8	D0	H/L	Display Data
9	D1	H/L	Display Data
10	D2	H/L	Display Data
11	D3	H/L	Display Data
12	D4	H/L	Display Data
13	D5	H/L	Display Data
14	D6	H/L	Display Data
15	D7	H/L	Display Data
16	VSS	-	GND

② PIN ASSIGNMENT OF CCFL CONNECTION

Pin No.	SYMBOL	LEVEL	FUNCTION
1	VCFL	-	Power Supply for CFL
2	NC	-	No Connection
3	NC	-	No Connection
4	VSS	-	GND for CFL



NOTE :

1. RESOLUTION : 320 x 3(R.G.B.) x 240 DOTS
2. CONTROLLER : EXCLUDED
3. DC/DC CONVERTER : INCLUDED
4. ① INTERFACE CONNECTOR
FPC, N16 P1.0mm
- ② CCFL CONNECTOR
JAE/II-G-4S-S3C2 or COMPATIBLE
5. TOLERANCE NO SPECIFIED : ± 0.5mm

REV. NO.	DESCRIPTION	DATE	DESIGN	CHECK	APPROVE

南亞塑膠工業股份有限公司
NAN YA PLASTICS CORPORATION
製品圖

LCBHBTB61M63S

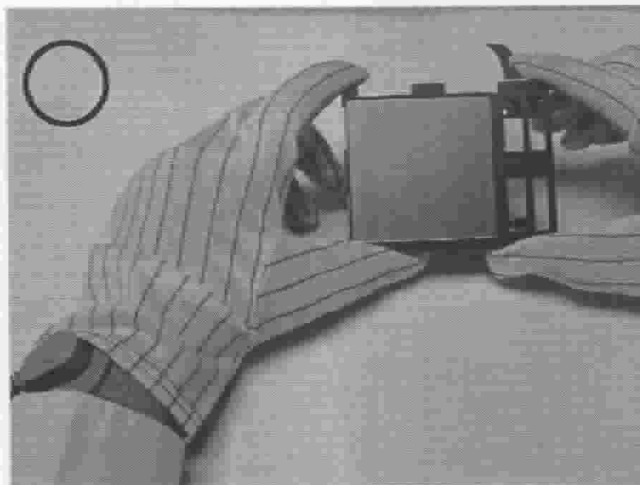
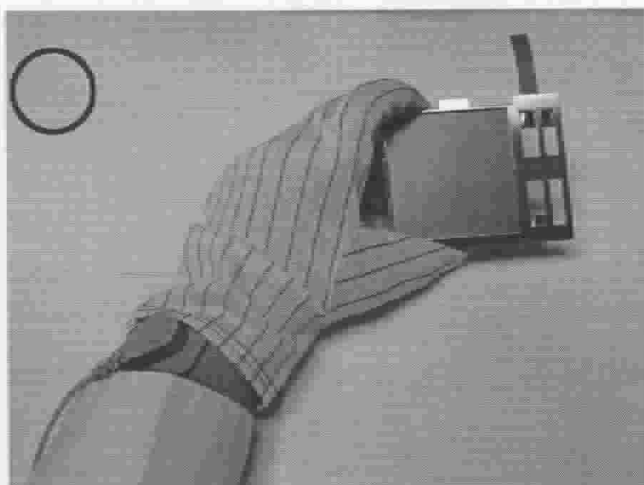
APPROVE	NAME	DATE	THIRD ANGLE P.
CHECK			
DESIGN	Campos Chen	95.03.07	SCALE
DRAWN	Campos Chen	95.03.07	1/1
DWG NO.	M B 6 1 A D 63 A		

THE NOTES OF LCM USING

LCM is easy to damage.

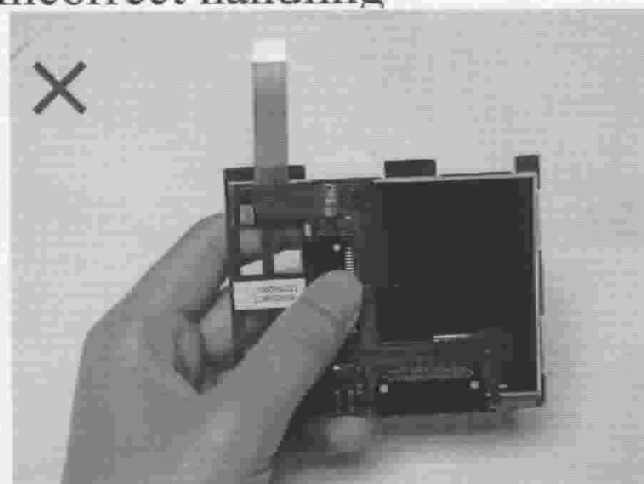
Please follow the notes as bellows, and be careful of handling!

Correct handling

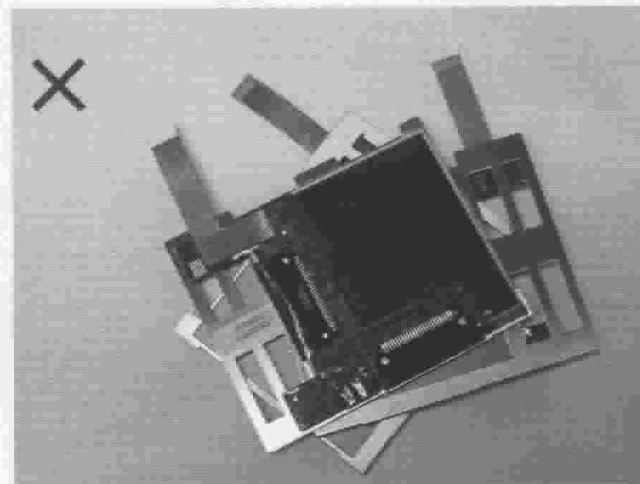


As above picture, please handle with glove by LCM edges and full EOS/ESD protection.

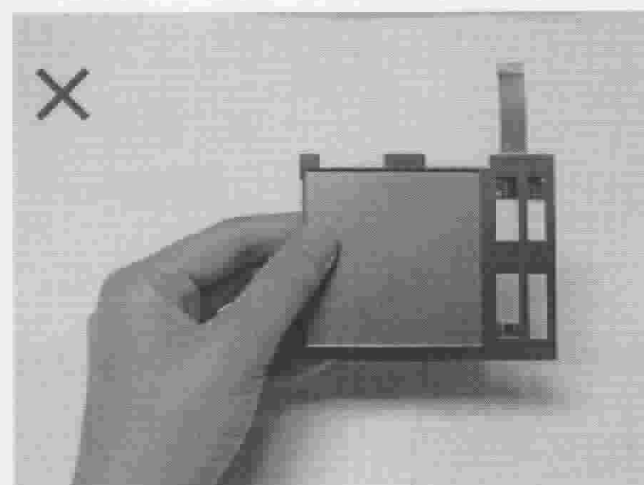
Incorrect handling



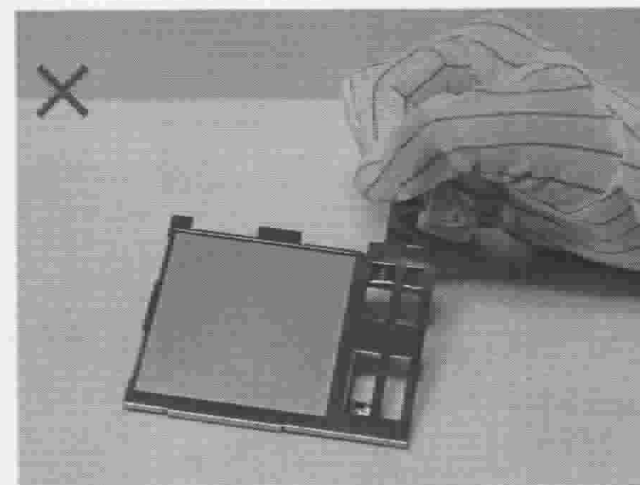
Please don't touch IC directly.



Please don't put one on another LCM.



Please don't hold the surface of LCM.



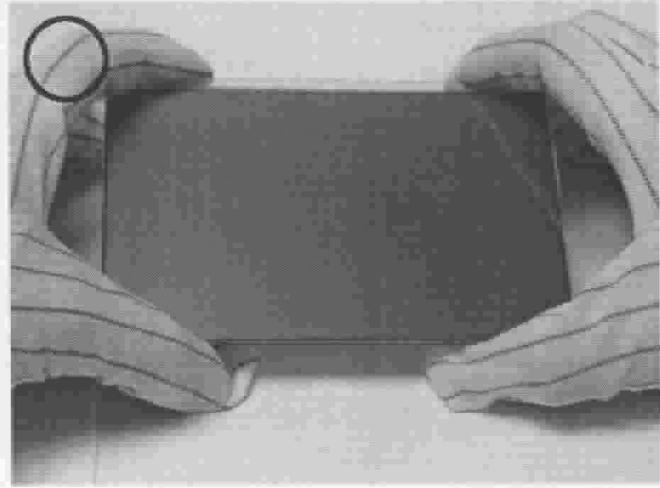
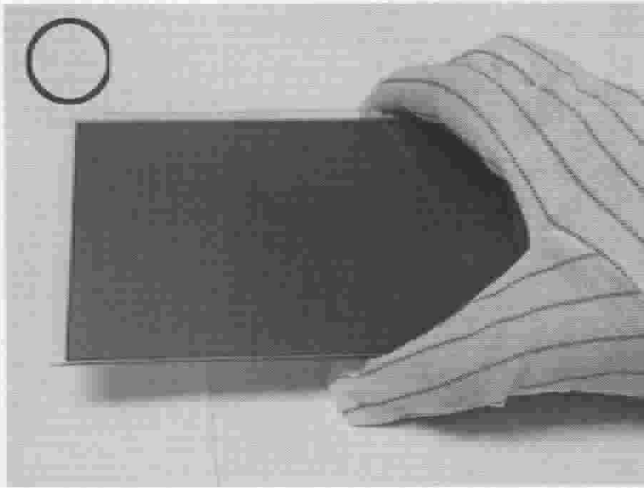
Please don't stretch interface of output.

THE NOTES OF LCD USING

LCD is easy damage.

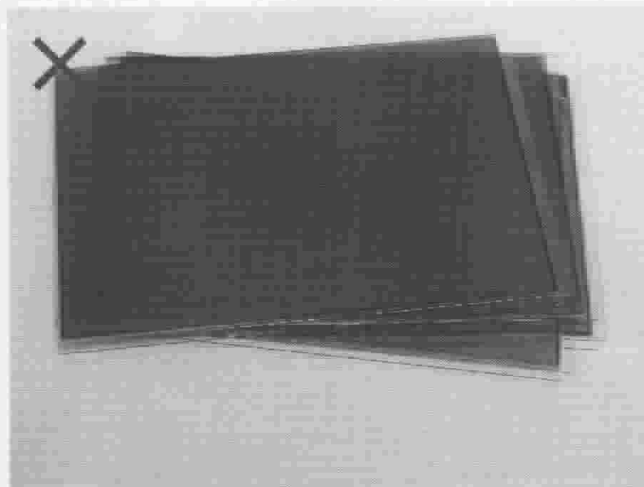
Please follow notes as bellows, and be careful of handling!

Correct handling

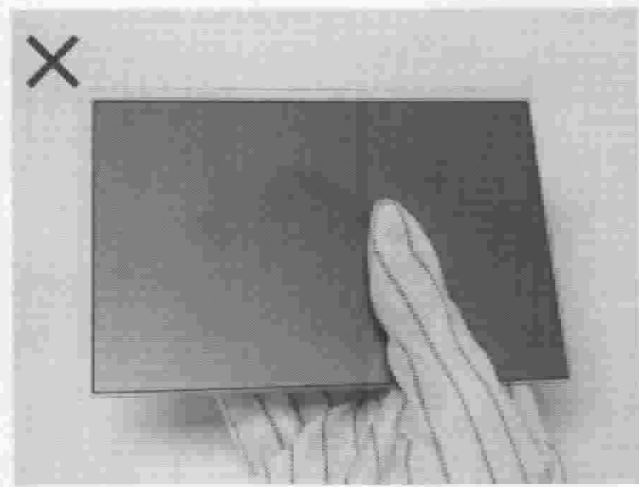


As above picture, please handle with glove by LCD edges and full EOS/ESD protection.

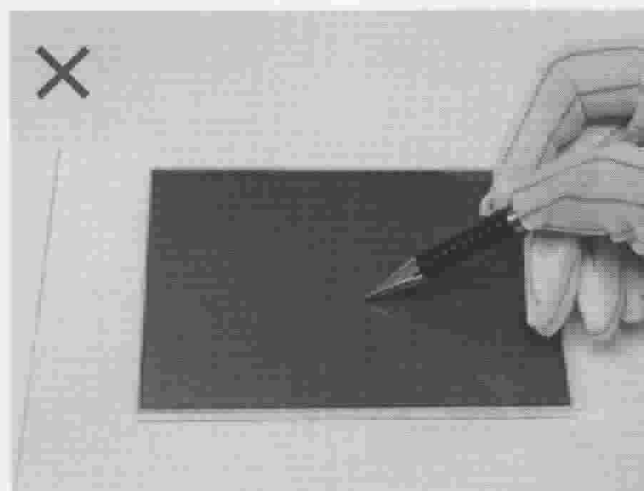
Incorrect handling



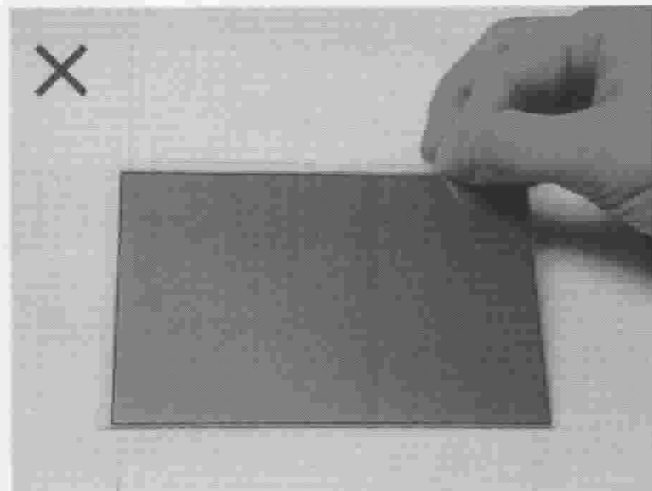
Please don't put one on another LCD.



Please don't hold the surface of LCD.



Please don't operate with sharp stick such as sharp pencil.



Please don't touch ITO glass without anti-static gloves.

